

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Original) An apparatus comprising:  
core control logic to provide a data signal; and  
output drive logic including a local data latch and a transmitter, the data latch to receive the data signal and to provide true and complementary forms of the data signal to the transmitter over symmetrical signal paths, the transmitter to provide an output signal to an external receiver.
2. (Original) The apparatus of claim 1 wherein true and complementary signal paths between the local data latch and the transmitter are symmetrical.
3. (Original) The apparatus of claim 1 further comprising  
design for testability (DFT) logic local to the transmitter, the DFT logic including an internal receiver coupled to an output of the transmitter and a local DFT latch coupled to an output of the internal receiver.
4. (Original) The apparatus of claim 3 wherein the internal receiver comprises a dual oxide sense amplifier.

5. (Original) The apparatus of claim 4 wherein the dual oxide sense amplifier includes precharge logic to precharge input lines to the dual oxide sense amplifier to a first voltage level.

6. (Original) The apparatus of claim 5 wherein the dual oxide sense amplifier is clocked.

7. (Original) The apparatus of claim 5 wherein the dual oxide sense amplifier is capable of operating in response to low voltage swing, differential input signals.

8.-29. (Canceled)